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OCT 14 2004

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kourosh Gharachorloo et al.	§	Confirmation No.:	9440
Serial No.: 10/672,960	§	Group Art Unit:	2186
Filed: 09/26/2003	§	Examiner:	Unknown
For: System And Method For Limited Fanout Daisy Chaining Of Cache Invalidation Requests In A Shared- Memory Multiprocessor System	§	Docket No.:	200302257-2

**TRANSMITTAL OF DECLARATION**

Mail Stop Missing Parts  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Date: October 14, 2004

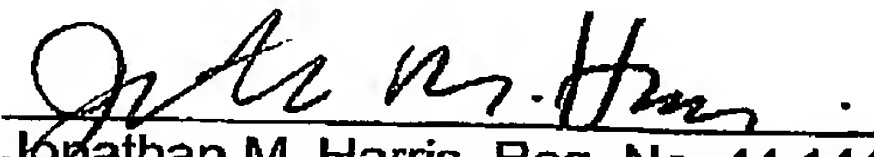
Sir:

At the time the above-identified application was filed, a Declaration signed by Andreas Nowatzky was not submitted. As of this date, we have not received a Notice to File Missing Parts. As we should have received such Notice, enclosed is a Declaration signed by Andreas Nowatzky.

It is believed that no extensions of time or fees are required, beyond those that may otherwise be provided for in documents accompanying this paper. However, in the event that additional extensions of time are necessary to allow consideration of this paper, such extensions are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required (including fees for net addition of claims) are hereby authorized to be charged to Hewlett-Packard Development Company's Deposit Account No. 08-2025.

Respectfully submitted,

HEWLETT-PACKARD COMPANY  
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ATTORNEY FOR APPLICANTS

PENNIE &amp; EDMONDS LLP DOCKET NO. 9772-0329-999

**DECLARATION FOR NON-PROVISIONAL PATENT APPLICATION\***

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below at 201 et seq. beneath my name.

I believe I am the original, first and sole inventor if only one name is listed at 201 below, or an original, first and joint inventor if plural names are listed at 201 et seq. below, of the subject matter which is claimed and for which a patent is sought on the invention entitled

**SYSTEM AND METHOD FOR LIMITED FANOUT DAISY CHAINING OF CACHE OF INVALIDATION REQUESTS  
IN A SHARED-MEMORY MULTIPROCESSOR SYSTEM**

and for which a patent application:

☐ is attached hereto and includes amendment(s) filed on *(if applicable)*

☒ was filed in the United States on June 11, 2001 as Application No. 09/878,985

with amendment(s) filed on *(if applicable)*

☐ was filed as PCT international Application No. on and was amended under PCT Article 19 on *(if applicable)*

I hereby state that I have reviewed and understand the contents of the above identified application, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

EARLIEST FOREIGN APPLICATION(S), IF ANY, FILED PRIOR TO THE FILING DATE OF THE APPLICATION			
APPLICATION NUMBER	COUNTRY	DATE OF FILING (day, month, year)	PRIORITY CLAIMED
			YES <input type="checkbox"/> NO <input type="checkbox"/>
			YES <input type="checkbox"/> NO <input type="checkbox"/>
			YES <input type="checkbox"/> NO <input type="checkbox"/>

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below.

PROVISIONAL APPLICATION NUMBER	FILING DATE
60/210,675	June 10, 2000

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose information known to me which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

NON-PROVISIONAL APPLICATION SERIAL NO.	FILING DATE	STATUS		
		PATENTED	PENDING	ABANDONED

\* for use only when the application is assigned to a company, partnership or other organization.

(1)

CAI - 276311.1

PENNIE &amp; EDMONDS LLP DOCKET NO. 9772-0329-999

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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	SIGNATURE OF INVENTOR 205			DATE	

(2)

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